

PATENT

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[TS03-0119]**II. Amendments to the Specification**

Please amend Paragraph 9 as set forth below:

[0009] Fig. 2 illustrates a preferred embodiment of a circuit for ESD protection. The circuit comprises pad 210, functional circuit 250, ESD protection circuit 220 operatively coupled to pad 210 and functional circuit 250, first resistive device 230 coupled to pad 210 and functional circuit 250, and active device 240 operatively coupled to first resistive device 230 and ESD protection circuit 220. In one embodiment, the active device 240 is an NMOS transistor having a width from about 10  $\mu\text{m}$  to about 30  $\mu\text{m}$  and has a length from about 0.15  $\mu\text{m}$  to about 0.25  $\mu\text{m}$ .